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“Tutorial – Simulation and code generation of TI InstaSPIN using DRV8305 EVM.pdf”. This tutorial describes, in step by step, how to set up and perform PIL simulation using the general PIL block. The process involves the following steps: 1. Preparing the code for PIL simulation 2. Setting up in PSIM 3. TUTORIAL Processor-In-the-Loop Simulation In this video I have explained how to generate C code from the developed model. In this video I have explained how to generate C code

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The Simulation generates code based on the StateMachine model. That model can have triggers. These triggers can be set

to be from external devices like PLCs, but you do need to alter the code generated to set your interface to them. See the section Leveraging existing code, in the Help topic Code Generation for Executable StateMachines.

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