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# Digital Circuit Design For Computer Science Students An Introductory Textbook 1st Edition

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Top 10 Books for Computer Engineers \u0026amp; Hardware Engineers Digital Circuits and Design by Pearson What Is DIGITAL LOGIC DESIGN? | How is it related to Circuits? | EXPLAINED Logic Gates, Truth Tables, Boolean Algebra AND, OR, NOT, NAND \u0026amp; NOR EEVblog #1270 - Electronics Textbook Shootout

ARM Edition

Digital Logic Circuit Analysis and Design (second Edition)

Analog Circuit Design

CMOS Logic Circuit Design

From Logic Gates to Processors

Basic Concepts and Principles

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Principles and Practice  
Advances in Synthetic Biology  
Digital Electronics: A Primer - Introductory Logic Circuit Design  
Computer-aided Design of Microelectronic Circuits and Systems: Digital-circuit  
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*Digital Circuit  
Design For  
Computer  
Science  
Students An  
Introductory  
Textbook 1st  
Edition*

*OMB No.  
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edited by*

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**MAHONEY MERCER**

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**ARM Edition** Springer  
Nature  
A COMPREHENSIVE GUIDE

TO THE DESIGN &  
ORGANIZATION OF  
MODERN COMPUTING  
SYSTEMS Digital Logic  
Design and Computer

Organization with Computer Architecture for Security provides practicing engineers and students with a clear understanding of computer hardware technologies. The fundamentals of digital logic design as well as the use of the Verilog hardware description language are discussed. The book covers computer organization and architecture, modern design concepts, and computer security through hardware. Techniques for designing

both small and large combinational and sequential circuits are thoroughly explained. This detailed reference addresses memory technologies, CPU design and techniques to increase performance, microcomputer architecture, including "plug and play" device interface, and memory hierarchy. A chapter on security engineering methodology as it applies to computer architecture concludes the book. Sample problems, design examples, and detailed

diagrams are provided throughout this practical resource. COVERAGE INCLUDES: Combinational circuits: small designs Combinational circuits: large designs Sequential circuits: core modules Sequential circuits: small designs Sequential circuits: large designs Memory Instruction set architecture Computer architecture: interconnection Memory system Computer architecture: security

## DIGITAL LOGIC CIRCUIT

## **ANALYSIS AND DESIGN (SECOND EDITION)**

MIT Press

This textbook is intended to introduce the student of electronics to the fundamentals of digital circuits, both combinational and sequential, in a reasonable and systematic manner. It proceeds from basic logic concepts to circuits and designs.

### **Analog Circuit Design**

Cambridge University Press

This volume concentrates

on three topics: mixed analog--digital circuit design, sensor interface circuits and communication circuits. The book comprises six papers on each topic of a tutorial nature aimed at improving the design of analog circuits. The book is divided into three parts. Part I: Mixed Analog--Digital Circuit Design considers the largest growth area in microelectronics. Both standard designs and ASICs have begun integrating analog cells and digital sections on the

same chip. The papers cover topics such as groundbounce and supply-line spikes, design methodologies for high-level design and actual mixed analog--digital designs. Part II: Sensor Interface Circuits describes various types of signal conditioning circuits and interfaces for sensors. These include interface solutions for capacitive sensors, sigma-delta modulation used to combine a microprocessor compatible interface with on chip CMOS sensors, injectable sensors and

responders, signal conditioning circuits and sensors combined with indirect converters. Part III: Communication Circuits concentrates on systems and implemented circuits for use in personal communication systems. These have applications in cordless telephones and mobile telephone systems for use in cellular networks. A major requirement for these systems is low power consumption, especially when operating in standby mode, so as to maximise the time

between battery recharges.  
CMOS Logic Circuit Design  
Academic Press  
This textbook for core courses in Electronic Circuit Design teaches students the design and application of a broad range of analog electronic circuits in a comprehensive and clear manner. Readers will be enabled to design complete, functional circuits or systems. The authors first provide a foundation in the theory and operation of basic electronic devices,

including the diode, bipolar junction transistor, field effect transistor, operational amplifier and current feedback amplifier. They then present comprehensive instruction on the design of working, realistic electronic circuits of varying levels of complexity, including power amplifiers, regulated power supplies, filters, oscillators and waveform generators. Many examples help the reader quickly become familiar with key design parameters and design

methodology for each class of circuits. Each chapter starts from fundamental circuits and develops them step-by-step into a broad range of applications of real circuits and systems. Written to be accessible to students of varying backgrounds, this textbook presents the design of realistic, working analog electronic circuits for key systems; Includes worked examples of functioning circuits, throughout every chapter, with an emphasis on real applications; Includes

numerous exercises at the end of each chapter; Uses simulations to demonstrate the functionality of the designed circuits; Enables readers to design important electronic circuits including amplifiers, power supplies and oscillators. From Logic Gates to Processors Academic Press  
This book presents the basic concepts used in the design and analysis of digital systems and introduces the principles of digital computer

organization and design. *Basic Concepts and Principles* Springer  
The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books, this RISC-V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC-V microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the

fundamentals of digital logic to the actual design of a processor. By the end of this book, readers will be able to build their own RISC-V microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing a RISC-V processor. SystemVerilog and VHDL are integrated throughout the text in

examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in

digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC-V microprocessor Gives students a full understanding of the RISC-V instruction set architecture, enabling them to build a RISC-V processor and program the RISC-V processor in hardware simulation, software simulation, and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as

of single-cycle, multicycle, and pipelined versions of the RISC-V architecture. Features a companion website with a bonus chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects,

and solutions to exercises. See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems. [Foundation of Digital Electronics and Logic Design](#) Springer Science & Business Media. A completely updated and expanded comprehensive treatment of VHDL and its applications to the design and simulation of real, industry-standard circuits. This comprehensive treatment of VHDL and its applications to the design and simulation of real,

industry-standard circuits has been completely updated and expanded for the third edition. New features include all VHDL-2008 constructs, an extensive review of digital circuits, RTL analysis, and an unequalled collection of VHDL examples and exercises. The book focuses on the use of VHDL rather than solely on the language, with an emphasis on design examples and laboratory exercises. The third edition begins with a detailed review of digital circuits (combinatorial,



sequential, state machines, and FPGAs), thus providing a self-contained single reference for the teaching of digital circuit design with VHDL. In its coverage of VHDL-2008, it makes a clear distinction between VHDL for synthesis and VHDL for simulation. The text offers complete VHDL codes in examples as well as simulation results and comments. The significantly expanded examples and exercises include many not previously published, with multiple physical

demonstrations meant to inspire and motivate students. The book is suitable for undergraduate and graduate students in VHDL and digital circuit design, and can be used as a professional reference for VHDL practitioners. It can also serve as a text for digital VLSI in-house or academic courses.

### **PRINCIPLES AND PRACTICE**

Springer Nature  
For junior-senior- and graduate-level courses in

digital circuits and digital electronics. Focused on the analog analysis of digital circuits, this text bridges the gap between theory and the actual design of practical high-speed, high-density computer circuits -- with an emphasis on the various noises that the designer must be aware of in order to identify preventive measures and make appropriate trade-offs.

Elsevier  
The author is the leading programming language

designer of our time and in this book, based on a course for 2nd-year students at, he closes the gap between hardware and software design. He encourages students to put the theory to work in exercises that include lab work culminating in the design of a simple yet complete computer. In short, a modern introduction to designing circuits using state-of-the-art technology and a concise, easy to master hardware description language (Lola).  
*Advances in Synthetic*

*Biology* Digital Circuit Design for Computer Science Students An Introductory Textbook Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work. The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years. The second edition of Digital Integrated Circuits: Analysis and Design

focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come. Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication, this book delves into the dramatic advances in the field, including new applications and changes in the physics of operation made possible by relentless miniaturization. This book was conceived in the

versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic. Like the first edition, this volume is a crucial link for integrated circuit engineers and those studying the field, supplying the cross-disciplinary connections they require for guidance in more advanced work. For pedagogical reasons, the author uses SPICE level 1 computer simulation models but

introduces BSIM models that are indispensable for VLSI design. This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models. With four new chapters, more than 200 new illustrations, numerous worked examples, case studies, and support provided on a dynamic website, this text significantly expands concepts presented in the first edition. Digital Electronics: A

Primer - Introductory Logic Circuit Design  
Springer Science & Business Media

This book presents an excellent collection of contributions addressing different aspects of high-level synthesis from both industry and academia. It includes an overview of available EDA tool solutions and their applicability to design problems.

**Computer-aided Design of Microelectronic Circuits and Systems: Digital-circuit aspects and state of the art**

Springer Science & Business Media  
 As the costs of power and timing become increasingly difficult to manage in traditional synchronous systems, designers are being forced to look at asynchronous alternatives. Based on reworked and expanded papers from the VII Banff Higher Order Workshop, this volume examines asynchronous methods which have been used in large circuit design, ranging from initial formal specification to more

standard finite state machine based control models. Written by leading practitioners in the area, the papers cover many aspects of current practice including practical design, silicon compilation, and applications of formal specification. It also includes a state-of-the-art survey of asynchronous hardware design. The resulting volume will be invaluable to anyone interested in designing correct asynchronous circuits which exhibit high performance or low power

operation.

## **ELECTRONIC CIRCUIT DESIGN**

Pearson Education India  
 With growing consumer demand for portability and miniaturization in electronics, design engineers must concentrate on many additional aspects in their core design. The plethora of components that must be considered requires that engineers have a concise understanding of each aspect of the design process in order to prevent bug-laden

prototypes. Electronic Circuit Design allows engineers to understand the total design process and develop prototypes which require little to no debugging before release. It provides step-by-step instruction featuring modern components, such as analog and mixed signal blocks, in each chapter. The book details every aspect of the design process from conceptualization and specification to final implementation and release. The text also demonstrates how to

utilize device data sheet information and associated application notes to design an electronic system. The hybrid nature of electronic system design poses a great challenge to engineers. This book equips electronics designers with the practical knowledge and tools needed to develop problem free prototypes that are ready for release.

### **AN INTRODUCTORY TEXTBOOK**

Cambridge University Press

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### **DIGITAL COMPUTER DESIGN**

CRC Press  
This book takes an authoritative introduction to basic principles of digital design and practical requirements in both board-level and VLSI systems. Digital Design covers the most

widespread logic design practices while building a solid foundation of theoretical and engineering principles. This easy-to-follow book uses a practical writing style. Includes low voltage and LVCMOS/LVTTL. Coverage of Complex Programmable Logic Devices (CPLDs) and Field-Programmable Gate Arrays (FPGAs). Introduction of HDL-based digital design Covers VHDL as well as ABEL. Including simulation and synthesis.  
*A Rigorous Approach* CRC

Press  
In the past few years, reliable hardware system design has become increasingly important in the computer industry. Digital Circuit Testing and Testability is an easy to use introduction to the practices and techniques in this field. Parag K. Lala writes in a user-friendly and tutorial style, making the book easy to read, even for the newcomer to fault-tolerant system design. Each informative chapter is self-contained, with little or no previous knowledge of a topic

assumed. Extensive references follow each chapter, making further research in a particular area readily available. Each chapter covers a different aspect or technological component of fault-tolerant system design, and this book is an excellent compilation of up-to-date information in an area where such a book is needed.  
*Digital Circuit Design for Computer Science Students* Morgan Kaufmann  
The newest addition to the Harris and Harris

family of Digital Design and Computer Architecture books, this RISC-V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC-V microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of a processor. By the end of this book, readers will be able to build their own

RISC-V microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing a RISC-V processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a

chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts

through the design of a RISC-V microprocessor Gives students a full understanding of the RISC-V instruction set architecture, enabling them to build a RISC-V processor and program the RISC-V processor in hardware simulation, software simulation, and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single-cycle, multicycle, and pipelined versions of the RISC-V architecture Features a companion website with a bonus

chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive

problems  
Digital Circuit Design  
 Springer Science & Business Media  
 New, updated and expanded topics in the fourth edition include: EBCDIC, Grey code, practical applications of flip-flops, linear and shaft encoders, memory elements and FPGAs. The section on fault-finding has been expanded. A new chapter is dedicated to the interface between digital components and analog voltages. \*A highly accessible, comprehensive and fully



up to date digital systems text \*A well known and respected text now revamped for current courses \*Part of the Newnes suite of texts for HND/1st year modules  
**Digital Design and Computer Architecture**  
World Scientific Publishing Company  
Designers of high-speed integrated circuits face a bewildering array of choices and too often spend frustrating days tweaking gates to meet speed targets. Logical Effort: Designing Fast CMOS Circuits makes high

speed design easier and more methodical, providing a simple and broadly applicable method for estimating the delay resulting from factors such as topology, capacitance, and gate sizes. The brainchild of circuit and computer graphics pioneers Ivan Sutherland and Bob Sproull, "logical effort" will change the way you approach design challenges. This book begins by equipping you with a sound understanding of the method's essential

procedures and concepts- so you can start using it immediately. Later chapters explore the theory and finer points of the method and detail its specialized applications. Features Explains the method and how to apply it in two practically focused chapters. Improves circuit design intuition by teaching simple ways to discern the consequences of topology and gate size decisions. Offers easy ways to choose the fastest circuit from among an array of potential

circuit designs. Reduces the time spent on tweaking and simulations- so you can rapidly settle on a good design. Offers in-depth coverage of specialized areas of application for logical effort: skewed or unbalanced gates, other circuit families (including pseudo-NMOS and domino), wide structures such as decoders, and irregularly forking circuits. Presents a complete derivation of the method- so you see how and why it works.

Analog Circuit Design

Elsevier

This textbook, based on the author's fifteen years of teaching, is a complete teaching tool for turning students into logic designers in one semester. Each chapter describes new concepts, giving extensive applications and examples. Assuming no prior knowledge of discrete mathematics, the authors introduce all background in propositional logic, asymptotics, graphs, hardware and electronics. Important features of the

presentation are: • All material is presented in full detail. Every designed circuit is formally specified and implemented, the correctness of the implementation is proved, and the cost and delay are analyzed • Algorithmic solutions are offered for logical simulation, computation of propagation delay and minimum clock period • Connections are drawn from the physical analog world to the digital abstraction • The language of graphs is

used to describe formulas and circuits • Hundreds of figures, examples and exercises enhance

understanding. The extensive website (<http://www.eng.tau.ac.il/>)

~guy/Even-Medina/) includes teaching slides, links to Logisim and a DLX assembly simulator.

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